PTO/SB/21 (04-04)

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TRANSMITTAL
FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/763,858
Filing Date	January 21, 2004
First Named Inventor	Haruki ITO
Art Unit	2812
Examiner Name	Cheung Lee
Attorney Docket Number	9319I-000651

ENCLOSURES (check all that apply)								
Fee Transmittal F	orm	Drawing(s)		After Allowance Communication to Technology Center (TC)				
Fee Attached		Licensing-r	elated Papers	Appeal Communication to Board of Appeals and Interferences				
Amendment / Reply		Petition		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)				
After Final		Petition to Convert to a Provisional Application		Proprietary Information				
Affidavits/declaration(s)		Power of Attorney, Revocation Change of Correspondence Address		Status Letter				
Extension of Time Request		Terminal Disclaimer		Other Enclosure(s) (please identify below):				
Express Abandonment Request		Request for Refund CD, Number of CD(s)		Acknowledgement Postcard				
Information Disclosure Statement								
Certified Copy of Priority Document(s) Response to Missing Parts/		Remarks The Commissioner is hereby authorized to charge any additional fees that may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 50-3213 (Epson R & D). A duplicate copy of this sheet is enclosed.						
Incomplete Applic								
Response to Parts under 3 1.52 or 1.53	•							
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT								
Firm or Individual name	Harness, Dickey &	-	Attorney Name G. Gregory Schivley Bryant E Wade	Reg. No. 27,382 40,344				
Signature	J. Theypay Ohnoles							
Date	December 7, 2005							
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Signature // / My / Mwky Date December 7, 2005	Typed or printed name	G. Gregory Schwie	y B	ryant E. Wade	/h/·//	Express Mail Label No.	EV 717 344 446 US
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.

10/763,858

Filing Date:

January 21, 2004

Applicant:

Haruki ITO

Group Art Unit:

2812

Examiner:

Cheung Lee

Title:

SEMICONDUCTOR DEVICE AND METHOD OF

MANUFACTURING THE SAME, SEMICONDUCTOR WAFER, CIRCUIT BOARD AND ELECTRONIC

INSTRUMENT

Attorney Docket:

93191-000651

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

<u>AMENDMENT</u>

Sir:

In response to the Office Action mailed September 7, 2005, please amend the application as follows and consider the remarks set forth below.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 7 of this paper.